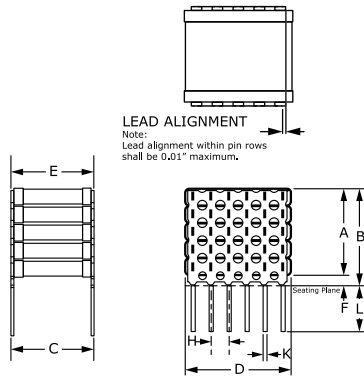


KPS-MCC Indust COG HT200C, Ceramic, 200C, 0.056 uF, 10%, 630 VDC, COG



| Dimensions | |
|------------|-------------------|
| D | 6.35mm +/-0.635mm |
| L | 6.35mm MIN |
| H | 2.54mm NOM |
| F | 1.397mm +/-0.25mm |
| A | 5.3mm MAX |
| B | 7.078mm MAX |
| C | 6.35mm +/-0.635mm |
| E | 7.62mm MAX |
| K | 0.5mm NOM |

| Packaging Specifications | |
|--------------------------|----|
| Packaging Quantity: | 50 |

| General Information | |
|---------------------|---|
| Supplier: | KEMET |
| Series: | KPS-MCC Indust COG HT200C |
| Style: | Leaded Stacked Chip |
| Description: | Low ESR, Stacked Ceramic Chips |
| Features: | 200C, Low ESR, High Thermal Stability, Bulk Capacitance |
| RoHS: | With Exemptions |
| Termination: | Silver |
| Lead: | Straight Leads |
| Notes: | Note: Number Of Chips In Stack Depends On Design. Number Of Chips In This Stack = 2 |

| Specifications | |
|----------------------------------|---------------------|
| Capacitance: | 0.056 uF |
| Capacitance Tolerance: | 10% |
| Voltage DC: | 630 VDC |
| Dielectric Withstanding Voltage: | 819 V |
| Temperature Range: | -55/+200C |
| Temperature Coefficient: | COG |
| Dissipation Factor: | 0.10% 1kHz 25C |
| Aging Rate: | 0% Loss/Decade Hour |
| Insulation Resistance: | 17.86 GOhms |