## High Efficiency White LED Driver

## ■ GENERAL DESCRIPTION

## PACKAGE OUTLINE

The NJU6050 is a high-efficiency white LED driver. It contains a high-efficiency step-up DC/DC converter and an output driver. The IC ensures a 28 V maximum capacity which allows series connection of 8 white LEDs with a constant current so that the LED currents are identical for uniform brightness.

The high frequency of the step-up converter permits the use of small, low-profile inductors and capacitors to minimize the footprint in space-conscious applications. And additional features are a low operating voltage of 2.5 V and the small package SOT-23 (MTP-5).

All of these benefits make the NJU6050 suitable for the battery-powered portable applications such as a cellular phone, a camcorder, PDA, etc.

## FEATURES

- Drives Up to 8 White LEDs in Series

$$
\mathrm{V}_{\mathrm{SW}}=28 \mathrm{~V}, \mathrm{I}_{\mathrm{OUT}}=20 \mathrm{~mA}
$$

- Uses Small Inductor and Capacitors
- 2.5 V to 6.5 V Operating Voltage for Step-up Circuits $\left(\mathrm{V}_{\mathrm{DD}}\right)$
- CMOS Technology
- Package : SOT-23 5 pin (MTP-5)

PIN CONFIGURATION (TOP VIEW)


PIN DESCRIPTIONS

| No. | SYMBOL | TYPE | DESCRIPTION |
| :---: | :---: | :---: | :--- |
| 1 | SW | Input | Switch Terminal |
| 2 | $V_{S S}$ | Power | Ground Terminal |
| 3 | FB | Input | Feedback Terminal |
| 4 | SHDNb | Input | Shutdown Terminal Active "L" <br> "L": Shutdown <br> "H": Normal Operation |
| 5 | $\mathrm{~V}_{\mathrm{DD}}$ | Power | Power Supply terminal |

## BLOCK DIAGRAM



## FUNCTIONAL DESCRIPTONS

## (1) LED Current Control and Resistor R LEd Selection

The NJU6050 incorporates the LED current control to regulate the LED current ( $\mathrm{I}_{\text {LED }}$ ), which is programmed by the feedback resistor ( $\mathrm{R}_{\mathrm{LED}}$ ) connected between the FB and the $\mathrm{V}_{\mathrm{SS}}$ terminals. The reference voltage $\mathrm{V}_{\text {REF }}$ is internally regulated to 1.2 V typical and connected to the positive input of the built-in comparator. Formula (1) is used to choose the value of the $\mathrm{R}_{\text {LED }}$, as shown below.

$$
\begin{aligned}
& R_{L E D}=\frac{V_{R E F}}{I_{L E D}} \quad \text {--. Formula (1) } \\
& V_{R E F}=1.2 \mathrm{~V} \text { (typ.) }
\end{aligned}
$$

The $\mathrm{I}_{\text {LED }}$ is the constant current programmed by the $\mathrm{R}_{\text {LED }}$. When the feedback voltage on the FB terminal reaches above the reference voltage $\mathrm{V}_{\text {REF }}$ (i.e., $\mathrm{I}_{\text {LED }}$ is above the level programmed by $\mathrm{R}_{\text {LED }}$ ), the output capacitor C 2 delivers the $\mathrm{I}_{\text {LED }}$. Once the feedback voltage drops below the reference voltage (i.e., $\mathrm{I}_{\text {LED }}$ drops below the level programmed by the $\mathrm{R}_{\mathrm{LED}}$ ), the MOS switch is turned on, then the current of the inductor L1 begins increasing. When the switch current reaches 350 mA , the MOS switch is turned off, then the L1 delivers current to the output through the diode D1 as the inductor current drops. After that, the MOS switch is turned on again and the switch current increases up to 350 mA . This switching cycle continues until the $\mathrm{I}_{\text {LED }}$ reaches the level programmed by the $\mathrm{R}_{\text {LED }}$, then the $\mathrm{I}_{\text {LED }}$ current is maintained constant.

## (2) Inductor Selection

The following equation gives the optimum inductor value:

| $\mathrm{L}=\frac{\mathrm{V}_{\mathrm{OUT}}-\mathrm{V}_{\mathrm{IN(MIN)}+}+\mathrm{V}_{\mathrm{D}}}{\mathrm{I}_{\mathrm{CL}}} \mathrm{t}_{\mathrm{OFF}}$ | Formula (2) |
| :--- | :--- |
|  |  |
| VD | $:$ Schottky diode Voltage |
| $\mathrm{I}_{\mathrm{CL}}$ | :Switch current limit |

The power supply voltage $\mathrm{V}_{\text {IN }}$ may fluctuate in battery-powered applications. For this reason, the minimum voltage should be applied to the $\mathrm{V}_{\mathrm{IN}}$ in Formula (2).

The NJU6050 has about 100 ns of delay time ( $\mathrm{T}_{\text {DELAY }}$ ), which is defined as the period from the reach of the current limit 350 mA to the MOS-switch-off. The $\mathrm{T}_{\text {DELAY }}$ may cause an overshoot-inductor-current, which is called the peak current $\mathrm{I}_{\mathrm{L}, \text { PEAK }}$, and calculated by Formula (3). Therefore, it is recommended that an inductor with a rating twice of the $\mathrm{I}_{\mathrm{L}, \text { PEAK }}$ and a low DCR (DC resistance) be used for high efficiency.

$$
\begin{aligned}
& I_{L, \text { PEAK }}=I_{C L}+\frac{V_{I N(M A X)}+V_{D S}}{L} t_{\text {DELAY }} \quad \text { Formula (3) } \\
& \text { VDS : Drain-Source voltage of MOS switch (=ILImit*Ron) } \\
& \mathrm{V}_{\text {IN }(\operatorname{MAX})} \text { : Maximum of } \mathrm{V}_{\text {IN }} \text { Voltage }
\end{aligned}
$$

## (3) Diode Selection

A schottky diode with a low forward-voltage-drop and a fast switching-speed is ideal for the D1. And the D1 must have a rating greater than the output voltage and output current in the system.

## (4) Capacitor Selection

A low ESR (Equivalent Series Resistance) capacitor should be used as the output capacitor C2 to minimize output ripples. A multi-layer ceramic capacitor is the best selection for the NJU6050 application because of not only the low ESR but its small package. A ceramic capacitor is also recommended for the input decoupling-capacitor C 1 , and should be placed as close to the NJU6050 as possible.

## (5) Open-circuit Protection

The zener diode D2 works as an open-circuit protection. When the LED is disconnected from the circuit, the feedback voltage on the FB terminal is pulled down to 0 V , which makes the switching frequency maximum. Without the diode D2, the SW voltage exceeds the absolute maximum rating, and this voltage stress may cause a permanent damage to the IC.

The zener voltage must be greater than the maximum forward-voltage of the LED string.

ABSOLUTE MAXIMUMN RATINGS

| PARAMETERS | SYMBOL | CONDITIONS | RATINGS | UNIT |
| :---: | :---: | :---: | :---: | :---: |
| VDD Power Supply | $\mathrm{V}_{\mathrm{DD}}$ |  | -0.3 to +7.0 | V |
| SHDNb Terminal Voltage | $\mathrm{V}_{\text {SHDNb }}$ | SHDNb terminal | -0.3 to +7.0 | V |
| SW Terminal Voltage | $V_{\text {SW }}$ | SW terminal | -0.3 to +30.0 | V |
| FB Terminal Voltage | $V_{\text {FB }}$ | FB terminal | -0.3 to $\mathrm{V}_{\mathrm{DD}}$ | V |
| Power Dissipation | PD | MTP-5 | 200 | mW |
| Operating Temperature | $\mathrm{T}_{\text {opr }}$ |  | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | $\mathrm{T}_{\text {stg }}$ |  | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |

Note1) All voltages are relative to $\mathrm{V}_{\mathrm{SS}}=0 \mathrm{~V}$ reference.
Note2) Do not exceed the absolute maximum ratings, otherwise the stress may cause a permanent damage to the IC. It is also recommended that the IC be used in the range specified in the DC electrical characteristics, or the electrical stress may cause mulfunctions and impact on the reliability.

## ■ DC ELECTRICAL CHARACTERISTICS

| $\left(\mathrm{V}_{\mathrm{DD}}=3.6 \mathrm{~V}, \mathrm{~V}_{\text {SHDNb }}=3.6 \mathrm{~V}, \mathrm{~V}_{\mathrm{SS}}=0 \mathrm{~V}, \mathrm{Ta}=25^{\circ} \mathrm{C}\right)$ |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| PARAMETERS | SYMBOL | CONDITIONS | MIN. | TYP. | MAX. | Unit |
| $V_{\text {DD }}$ Power Supply | $\mathrm{V}_{\mathrm{DD}}$ |  | 2.5 |  | 6.5 | V |
| Quiescent Current | $\mathrm{I}_{\text {STBY1 }}$ | No switching |  | 20 | 30 | $\mu \mathrm{A}$ |
|  | IStBY2 | $\mathrm{V}_{\text {SHDNb }}=0 \mathrm{~V}$ |  | 0.1 | 1 |  |
| FB Comparator Trip point | $\mathrm{V}_{\text {FBP }}$ |  | 1.18 | 1.2 | 1.22 | V |
| Output Voltage Line Regulation | $\mathrm{V}_{\mathrm{K}}$ | $2.5 \mathrm{~V}<\mathrm{V}_{\text {IN }}<6.5 \mathrm{~V}$ |  | -0.05 |  | \%/V |
| FB Terminal Bias Current | $\mathrm{I}_{\text {FB }}$ | $\mathrm{V}_{\mathrm{FB}}=1.2 \mathrm{~V}$ |  | 30.0 | 80.0 | nA |
| Switch Off Time | toff | $\mathrm{V}_{\text {FB }}>1.0 \mathrm{~V}$ |  | 500 |  | ns |
|  |  | $\mathrm{V}_{\mathrm{FB}}<0.6 \mathrm{~V}$ |  | 1.6 |  | $\mu \mathrm{s}$ |
| Switch VDS(ON) | $\mathrm{V}_{\text {DS }}$ | $\mathrm{Isw}_{\text {w }}=300 \mathrm{~mA}$ |  | 250 | 350 | mV |
| Switch Current Limit | ICL |  | 300 | 350 | 400 | mA |
| SHDNb Terminal Current | ISHDNb |  |  | 0.1 | 1.0 | $\mu \mathrm{A}$ |
| SHDNb Input Voltage High | $\mathrm{V}_{\text {SHDNH }}$ |  | 0.9 |  |  | V |
| SHDNb Input Voltage Low | $\mathrm{V}_{\text {SHDNL }}$ |  |  |  | 0.25 | V |
| Switch Leakage Current | $\mathrm{I}_{\mathrm{L}}$ | Switch Off, V ${ }_{\text {sw }}=28 \mathrm{~V}$ |  | 0.01 | 5.0 | $\mu \mathrm{A}$ |

## TYPICAL PERFORMANCE

$$
\left(\mathrm{V}_{\mathrm{DD}}=3.6 \mathrm{~V}, \mathrm{~V}_{\text {SHDNb }}=3.6 \mathrm{~V}, \mathrm{~V}_{\mathrm{SS}}=0 \mathrm{~V}, \mathrm{~L}=10 \mu \mathrm{H}, \mathrm{Ta}=25^{\circ} \mathrm{C}\right)
$$

Output Voltage vs. Input Voltage


Efficiency vs. Load Current

$\mathrm{V}_{\mathrm{DS}(\mathrm{ON})}$ vs. Temperature


Output Voltage vs. Load Current


Quiescent Current vs. Temperature


Feedback Voltage vs. Temperature


$$
\left(\mathrm{V}_{\mathrm{DD}}=3.6 \mathrm{~V}, \mathrm{~V}_{\mathrm{SHDNb}}=3.6 \mathrm{~V}, \mathrm{~V}_{\mathrm{SS}}=0 \mathrm{~V}, \mathrm{~L}=10 \mu \mathrm{H}, \mathrm{Ta}=25^{\circ} \mathrm{C}\right)
$$

FB Bias Current vs. Temperature


Line Transient


Switch Current Limit vs. Temperature


Load Transient


TYPICAL APPLICATION CIRCUITS
(1) 8 white LEDs

(2) 8 white LEDs \& Dimming control by external PWM signal


Referential List of External Components

| Component |  | Supplier / Parts Number | Qty | Value |
| :---: | :---: | :---: | :---: | :---: |
| IC1 | LED Driver IC | NJRC / NJU6050F | 1 |  |
| L1 | Inductor | TDK / VLF3010AT-100MR49 | 1 |  |
| D1 | Schottky Diode | ROHM / RB160M-30 | $10 \mu \mathrm{H}$ |  |
| D2 | Zener Diode | ROHM / UDZS27B | 1 |  |
| C1 | Ceramic Capacitor | TAIYO YUDEN / JMK107 BJ475MA | 1 | $4.7 \mu \mathrm{~F} 6.3 \mathrm{~V}$ |
| C2 | Ceramic Capacitor | TAIYO YUDEN / GMK316BJ105 | 1 | $1 \mu \mathrm{~F} 35 \mathrm{~V}$ |
| RLED(R1) | Chip Resistor | Standard | 1 | $30 \Omega$ |
| R2 | Chip Resistor | Standard | 1 | $120 \mathrm{k} \Omega$ |
| R3 | Chip Resistor | Standard | 1 | $240 \mathrm{k} \Omega$ |
| R4 | Chip Resistor | Standard | 1 | $620 \mathrm{k} \Omega$ |
| LED1 to 8 | White LED | NICHIA / NSCW215T | 8 |  |

## MEMO

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