# GaAs MMIC SP4T NON-REFLECTIVE SWITCH, DC - 20 GHz 

## Typical Applications

The HMC-C071 is ideal for:

- Fiber Optics \& Broadband Telecom
- Microwave Radio \& VSAT
- Military Radios, Radar, \& ECM
- Test Instrumentation


## Functional Diagram



## Features <br> High Isolation: >42 dB up to 12 GHz $>32 \mathrm{~dB}$ up to 20 GHz <br> Low Insertion Loss: 2 dB @ 2 GHz 2.8 dB @ 12 GHz <br> Fast Switching: 17 ns Rise/Fall Times <br> Non-Reflective Design <br> Hermetically Sealed Module <br> Field Replaceable SMA connectors <br> $-55^{\circ} \mathrm{C}$ to $+85{ }^{\circ} \mathrm{C}$ Operating Temperature <br> General Description

The HMC-C071 is a general purpose broadband high isolation non-reflective GaAs pHEMT SP4T switch housed in a miniature hermetic module with field replaceable SMA connectors. Covering DC to 20 GHz , the switch offers high isolation and low insertion loss. The switch features $>42 \mathrm{~dB}$ isolation up to 12 GHz and $>32 \mathrm{~dB}$ isolation up to 20 GHz . The HMC-C071 also provides 2.8 dB insertion loss up to 12 GHz with very fast rise and fall times of 17 ns . A CMOS interface allows a single +5 V bias voltage at very low DC currents.

Electrical Specifications, $T_{A}=+25^{\circ} \mathrm{C}$, With Vdc $=+5 \mathrm{~V}$ \& 0/+5V Control, 50 Ohm System

| Parameter | Frequency | Min. | Typ. | Max. | Units |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss | $\begin{aligned} & \mathrm{DC}-6 \mathrm{GHz} \\ & \mathrm{DC}-12 \mathrm{GHz} \\ & \mathrm{DC}-20 \mathrm{GHz} \end{aligned}$ |  | $\begin{aligned} & -2.7 \\ & -2.8 \\ & -3.8 \end{aligned}$ | $\begin{gathered} -3.2 \\ -3.8 \\ -5 \end{gathered}$ | dB <br> dB <br> dB |
| Isolation | $\begin{aligned} & \mathrm{DC}-6 \mathrm{GHz} \\ & \mathrm{DC}-12 \mathrm{GHz} \\ & \mathrm{DC}-20 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & 44 \\ & 36 \\ & 35 \end{aligned}$ | $\begin{aligned} & 48 \\ & 42 \\ & 38 \end{aligned}$ |  | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \end{aligned}$ |
| Return Loss "On State" | $\begin{aligned} & \mathrm{DC}-12 \mathrm{GHz} \\ & \mathrm{DC}-20 \mathrm{GHz} \end{aligned}$ |  | $\begin{aligned} & 12 \\ & 10 \end{aligned}$ |  | $\mathrm{dB}$ $\mathrm{dB}$ |
| Return Loss RF1, RF2 "Off State" | $\begin{aligned} & \mathrm{DC}-12 \mathrm{GHz} \\ & \mathrm{DC}-20 \mathrm{GHz} \end{aligned}$ |  | $\begin{aligned} & 15 \\ & 10 \end{aligned}$ |  | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ |
| Input Power for 1 dB Compression | 0.5-20 GHz | 20.5 | 24 |  | dBm |
| Input Third Order Intercept <br> (Two-Tone Input Power $=+7 \mathrm{dBm}$ Each Tone) | 0.5-20 GHz | 36.5 | 40 |  | dBm |
| Switching Characteristics tRISE, tFALL (10/90\% RF) tON, tOFF (50\% CTL to $10 / 90 \%$ RF) | DC - 20 GHz |  | $\begin{gathered} 17 \\ 130 \end{gathered}$ |  | $\begin{aligned} & \text { ns } \\ & \text { ns } \end{aligned}$ |

Insertion Loss


Return Loss RF1, RF2, RF3, RF4 On


Isolations


Return Loss RFC


Return Loss RF1, RF2, RF3, RF4 Off


Isolation Between Ports RF1 and RF2

v04.0417
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Input P1dB Compression Point


Absolute Maximum Ratings

| RF Input Power | +24 dBm |
| :--- | :--- |
| Supply Voltage (Vdc) | +7 V |
| Control Voltage Range (Vctl) | -0.5 V to $\mathrm{Vdc}+1 \mathrm{~V}$ |
| Storage Temperature | -65 to $+150^{\circ} \mathrm{C}$ |
| Operating Temperature | -55 to $+85^{\circ} \mathrm{C}$ |

ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

Input Third Order Intercept Point


Control Voltages

| State | Bias Condition |
| :---: | :---: |
| High | +3.0 to Vdc @ 1 mA Typ. |
| Low | 0 to $+1.5 \mathrm{~V} @ 20 \mu \mathrm{~A}$ Typ. |

Truth Table

| Control Input | Signal Path State |  |
| :---: | :---: | :---: |
| VCTL1 | VCTL2 | RFC to: |
| LOW | LOW | RF1 |
| LOW | HIGH | RF2 |
| HIGH | LOW | RF3 |
| HIGH | HIGH | RF4 |

Bias Voltage \& Current

| Vdc Range $=+5 \mathrm{Vdc} \pm 10 \%$ |  |
| :---: | :---: |
| Vdc <br> $(\mathrm{V})$ | Idc (Typ.) <br> $(\mathrm{mA})$ |
| +5.0 | 1.4 |

(Bias current increases with switching rate to $15-20 \mathrm{~mA}$.)

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Pin Descriptions

| Pin Number | Function | Description | Interface Schematic |
| :---: | :---: | :---: | :---: |
| 1 | GND | Power supply ground. | $\begin{aligned} & \text { OGND } \\ & = \end{aligned}$ |
| 2, 3 | Vctl1, 2 | CMOS interface, control voltages per table. Requires active pull up to $+5 \mathrm{~V}\left(\mathrm{~V}_{\mathrm{dc}}\right)$. | (Internal Driver) |
| 4 | Vdc | Supply voltage |  |
| 5-9 | RFC, RF1, RF2, RF3, RF4 | RF connector, SMA female, field replaceable. These pins are DC coupled and matched to 50 Ohms. DC blocking capacitors are required if external RF line potential is not equal to 0 V . | RFC <br> RF1-RF4 |

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## Outline Drawing



Package Information

| Package Type | C-15 |
| :--- | :--- |

NOTES:

1. PACKAGE, LEADS, COVER MATERIAL: KOVARTM
2. FINISH: GOLD PLATE OVER NICKEL PLATE
3. ALL DIMENSIONS ARE IN INCHES [MILLIMETERS].
4. TOLERANCES:

$$
\text { 4.1 . } \mathrm{XX}= \pm .02[.51]
$$

$$
4.2 . \mathrm{XXX}= \pm .010[.25]
$$

5. MARK LOT NUMBER ON . 080 X .250 LABEL WHERE SHOWN, WITH . 030 MIN TEXT HEIGHT.
6. MOUNTING SPACER PART NUMBER: 123811.

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## Notes:

